B.Sc. (Part-II) Semester-IV Examination ELECTRONICS

COMMUNICATION ELECTRONICS AND 8085 MICROPROCESSOR

| Tim | e : T | hree | Hou | ırs] | | | | [Maximum Marks: | 80 | |
|----------------------------|-------|------------|-----------------------------|---|-----------------|-------------|---------------|-----------------|----|--|
| Note | e :— | (1) | All | questions are | compulsory. | | | | | |
| | | (2) | Drav | w neat diagra | am wherever ne | cessary. | | | | |
| 1. | (A) | Fill | in th | ne blanks : | | 2 | | | | |
| | | (i) | PC | stand for _ | · | | | | | |
| | | (ii) | BSR stand for | | | | | | | |
| | | (iii) | The | The width of data bus of 8085 microprocessor is | | | | | | |
| | | (iv) | FM | is | ħ.i. | | | | | |
| | (B) | Cho | ose o | correct alter | | 2 | | | | |
| | | (i) | AM is | | | | | | | |
| | | | (a) | Amplitude | modem | (b) | Amplitude m | odulation | | |
| | | | (c) | Frequency | modulation | (d) | None. | | | |
| | | (ii) | Mod | dulation is c | pposite process | s of | _• | | | |
| | | | (a) | Demodulat | ion | (b) | Rectification | | | |
| | | | (c) | Multiplexin | ng | (d) | None | | | |
| | | (iii) | The | re are | _ sidebands in | AM. | | | | |
| | | | (a) | One | | (b) | Two | | | |
| | | | (c) | Three | | (d) | Four | | | |
| | | (iv) | The | re are | _ flags in 808 | 5. | | | | |
| | | | (a) | 3 | | (b) | 8 | | | |
| | | | (c) | 5 | | (d) | 9 | | | |
| | (C) | | te answer in one sentence : | | | | | | 4 | |
| | | . , | | at is PPM? | | | | | | |
| | | 101.555.55 | | | ction of SP? | | | | | |
| (iii) What is fetch cycle? | | | | | | | 10000 | | | |
| | | (iv) | Giv | e the addres | ssing mode of S | STA 6500 | Н. | | | |
| | EIT | HEI | R | | | | | | | |
| 2. | | | | w the block diagram AM transmitter and explain the function of each blocks. | | | | | | |
| | (B) | Exp | olain | the FM the | ory and frequer | ncy spectru | ım of FM wav | e. | 6 | |
| | | | | | | | | | | |

| | OR | | | | | | | |
|----|------------|---|---------|--|--|--|--|--|
| | (P) | Draw and explain the block diagram of superheterodyne receiver. | 8 | | | | | |
| | (Q) | Explain the need of modulation in communication. | 4 | | | | | |
| | EIT | HER | | | | | | |
| 3. | 1000 | Explain the working of photodiode as an optical detector. | | | | | | |
| | (B) | Explain different types of optical fibers. | 6 | | | | | |
| | OR | | | | | | | |
| | (P) | Explain total Internal reflection and numerical aperture. | 6 | | | | | |
| | (Q) | Explain Laser diode as optical source in optical communication system. | 6 | | | | | |
| | EIT | HER | | | | | | |
| 4. | 1.00 | Explain the classification of pulse modulation. | 6 | | | | | |
| | | State and explain FDM with the help of suitable diagram. | 6 | | | | | |
| | OR | | 0 | | | | | |
| | | Explain PAM and PCM. | 8 | | | | | |
| | | What is quantization? Explain. | 4 | | | | | |
| E | | HER | | | | | | |
| 5. | (A) | Draw the block diagram of microcomputer and explain the function of each block | . 6 | | | | | |
| | (B) | 3) Explain the function of following pins in 8085 microprocessor: | | | | | | |
| | | (i) RD (ii) WR | | | | | | |
| | | (iii) IO/m (iv) HOLD | | | | | | |
| | | (v) READY (vi) D_0 to D_7 | 6 | | | | | |
| | OR | | | | | | | |
| | (P) | State and explain the general purpose registers of 8085 microprocessor. | | | | | | |
| | (Q) | Explain various status flags with suitable diagram of 8085 microprocessor. | 5 | | | | | |
| | (R) | Explain one byte, two byte and three byte instructions with suitable example. | 3 | | | | | |
| | EIT | THER | | | | | | |
| 6. | <i>a a</i> | What is addressing modes? Explain different types of addressing modes with suitable examples. | 6 | | | | | |
| | | Draw the flow chart and write ALP for addition of two 8-bit numbers using 80 microprocessor. | 85 6 | | | | | |
| | OR | | | | | | | |
| | | Explain the classification of instruction set of 8085 microprocessor. | 6 | | | | | |
| | | Draw the flow chart and write ALP for finding of maximum of two numbers. | 6 | | | | | |
| 7 | | THER | , | | | | | |
| 7. | | Draw the block diagram of 8255 PPI & explain the working of each block. | 6 | | | | | |
| | 0.00 | Explain memory mapped I/O and I/O mapped I/O scheme. | 6 | | | | | |
| | OR (P) | Explain control word format for BSR mode with suitable diagram and example. | 6 | | | | | |
| | 77-57 | • 5 5 5 5 5 5 5 5 5 5 | | | | | | |
| | (Q) | Explain various data transfer schemes. | 6 | | | | | |